

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:	) Group Art Unit:
Shunpei YAMAZAKI et al.	) Examiner:
Based On: JP 2002-276295	)
Filed On: September 20, 2002	)
For: DISPLAY DEVICE AND	)
MANUFACTURING METHOD	)
THEREOF	)

**INFORMATION DISCLOSURE STATEMENT**

Honorable Commissioner of Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

In accordance with the provisions of 37 C.F.R. 1.56 and 37 C.F.R. 1.97-1.99, Applicant submits herewith a Form PTO-1449 listing references known to Applicant and requests that these references be made of record in the above identified application. Copies of the references listed are submitted herewith in accordance with 37 C.F.R. 1.98(a). Copies of U.S. patents and U.S. publications are not enclosed in accordance with the Notice published in the Official Gazette on August 5, 2003 entitled *Information Disclosure Statements May Be Filed Without Copies of U.S. Patents and Published Applications in Patent Applications filed after June 30, 2003*, which waives the requirement under 37 CFR 1.98(a)(2)(i) for submitting a copy of each cited U.S. patent and each U.S. publication.

U.S. Patent Application Serial No. 10/245,701, which has issued as U.S. Publication No. 2003/0090002 on May 15, 2003, is related to the instant application.

JP 2000-58650 is in the family of U.S. Patent 6,475,912.

JP 2001-236025 is in the family of U.S. Publication No. 2001/0015618.

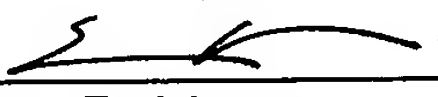
JP 2001-313397 is in the family of U.S. Publication No. 2001/0030322.

JP 2000-357671 is in the family of U.S. Patent 6,413,845.

This Information Disclosure Statement is being submitted before the issuance of a first Office Action on the merits, therefore, no fee is required.

The Commissioner is hereby authorized to charge fees under 37 C.F.R. §§1.16, 1.17, 1.20(a), 1.20(b), 1.20(c), and 1.20(d) (except the Issue Fee) which may be required now or hereafter, or credit any overpayment to Deposit Account No. 50-2280. A duplicate copy of this sheet is attached.

Respectfully submitted,

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PTO/SB/08A (08-00)

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Substitute for form 1449A/PTO				<b>Complete if Known</b>	
<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> <i>(use as many sheets as necessary)</i>				Application Number	Unassigned
				Filing Date	
				First Named Inventor	Shunpei YAMAZAKI et al.
				Group Art Unit	
				Examiner Name	
Sheet	2	of	2	Attorney Docket Number	0756-7199

U.S. PATENT DOCUMENTS						
Examiner Initials <sup>1</sup>	Cite No. <sup>1</sup>	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code <sup>2</sup> (if known)			
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FOREIGN PATENT DOCUMENTS							
Examiner Initials <sup>1</sup>	Cite No. <sup>1</sup>	Foreign Patent Document			Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Office <sup>3</sup>	Number <sup>4</sup>	Kind Code <sup>5</sup> (if known)			
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		JP	2001-313397			11/09/2001	AB
		JP	2002-353167			12/06/2002	AB
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OTHER PRIOR ART – NON PATENT LITERATURE DOCUMENTS				
Examiner Initials <sup>1</sup>	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.		
		Stanley WOLF, 4.5.6.3 Contact-Hole and Via Filling with Selective Electroless Metal Deposition, Silicon Processing for the VLSI Era, Volume 2, Process Integration, pp. 256-257.		
		Mutsuko HATANO et al., A Novel Self-Aligned Gate-Overlapped LDD Poly-Si TFT with High Reliability and Performance, IEDM, 1997, pp. 523-526.		
Examiner Signature				Date Considered

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

<sup>1</sup> Unique citation designation number. <sup>2</sup> See attached Kinds of U.S. Patent Documents. <sup>3</sup> Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). <sup>4</sup> For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. <sup>5</sup> Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. <sup>6</sup> Applicant is to place a check mark here if English language Translation is attached.

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				Filing Date		September 22, 2003	
				First Named Inventor		Shunpei YAMAZAKI et al.	
				Group Art Unit			
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Sheet	I	of	2	Attorney Docket Number		0756-7199	

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OTHER PRIOR ART – NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
		Tetsuo TSUTSUI et al., <i>High Quantum Efficiency in Organic Light-Emitting Devices with Iridium-Complex as a Triplet Emissive Center</i> , Jp. J. Appl. Phys., Vol. 38 (1999), pp. L1502, L1504, Part 2, No. 12B, 12/15/1999.	
		M.A. Baldo et al., <i>Very High-Efficiency Green Organic Light-Emitting Devices Based on Electrophosphorescence</i> , Applied Physics Letters, Volume 75, Number 1, 07/05/1999, pp. 4-6.	
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Examiner Signature		Date Considered	

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